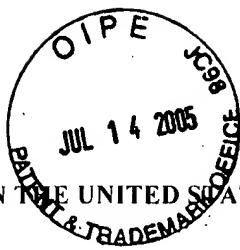


Docket No.: 049657-0875

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of

Customer Number: 20277

Tetsushi TANIZAKI, et al.

Confirmation Number: 8725

Application No.: 09/712,246

Group Art Unit: 2133

Filed: November 15, 2000

Examiner: M.M. Chaudry

For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT OPERATING AT HIGH RATE

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.  
☐ Applicant is entitled to small entity status under 37 CFR 1.27  
☐ Also attached:

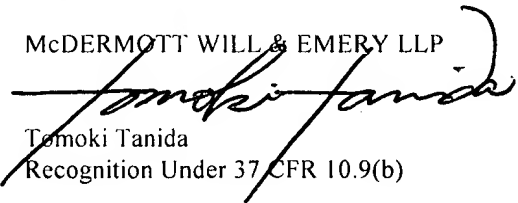
The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	4	20	0	\$50.00 =	\$0.00
Independent Claims	2	3	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

  
Tomoki Tanida

Recognition Under 37 CFR 10.9(b)

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 SAB/TT:lnm  
Facsimile: 202.756.8087

Date: July 14, 2005

WDC99 1106879-1.049657.0875

Please recognize our Customer No. 20277 as our  
correspondence address.

Docket No.: 049657-087



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Tetsushi TANIZAKI, et al.	:	Confirmation Number: 8725
Application No.: 09/712,246	:	Group Art Unit: 2133
Filed: November 15, 2000	:	Examiner: M.M. Chaudry
For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT OPERATING AT HIGH RATE		

**REQUEST FOR RECONSIDERATION**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Reconsideration of the rejection imposed in the Office Action dated April 19, 2005, is solicited in light of the following Remarks.